

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (USPTO)

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Title of Application	A FRACTIONAL-TYPE PHASE-LOCKED LOOP CIRCUIT WITH COMPENSATION OF PHASE ERRORS
First Named Inventor	Guido Gabriele ALBASINI and Enrico Temporiti
Assignee	STMicroelectronics, S.r.l. (large entity)
Group Art Unit	2611
Examiner	Khanh C. Tran
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CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being transmitted via the Office electronic filing system, EFS-Web, addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 4th day of January, 2011.

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